2700 INTERNAL TRANSFER REQUEST FOR S.N. 09/807614

DATE: 8/17/01	FROM: CAO (print name)

FORWARD TO:	REASON(S):	
A. Art Unit: 2183	A. You had Parent	(check box)
B. Class: 712	B. See Title	(check box)
C Subclass: 220†	C. See Abstract	(check box)
	D. See Claim(s):	

FURTHER EXPLANATION IF NEEDED:

masking an execution of processor instruction

DATE: 8/22/2001	FROM: Ellis, R (print name)

FORWARD TO:	REASON(S):	
A. Art Unit: 2131	A. You had Parent	(check box)
B. Class: 713	B. See Title	(check box)
C Subclass: 200	C. See Abstract	(check box)
	D. See Claim(s):	

FURTHER EXPLANATION IF NEEDED:

Tamper resistant processor

DATE:	FROM: (print name)

FORWARD TO CLASSIFIER	REASON(S):	
	A. You had Parent	(check box)
	B. See Title	(check box)
	C. See Abstract	(check box)
	D. See Claim(s):	

FURTHER EXPLANATION IF NEEDED:

DISPOSITION BY 2700 CLASSIFICATION

DATE:	CLASSIFIER:

FORWARD TO:	REASON(S):	
A. Art Unit:	A. You had Parent	(check box)
B. Class:	B. See Title	(check box)
C Subclass:	C. See Abstract	(check box)
	D. See Claim(s):	

FURTHER EXPLANATION IF NEEDED: